Heterogeneous Integration for HPC

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Special Focus Optics

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Heterogeneous Integration for HPC



SFRVFR

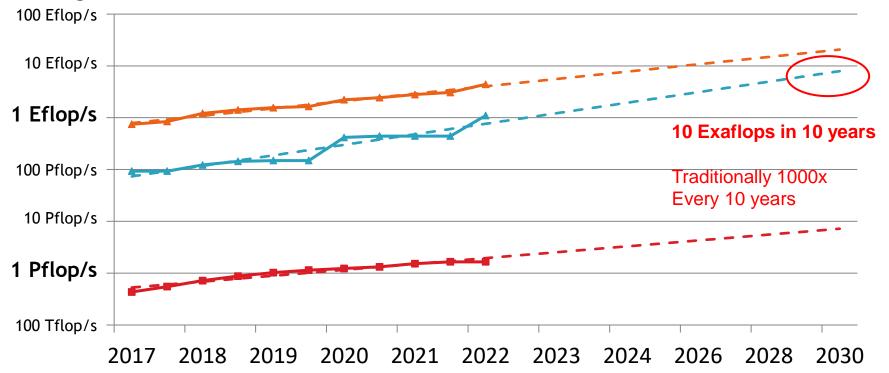
George Michelogiannakis Research Scientist Lawrence Berkeley National Laboratory



EMP



Projected Performance Development



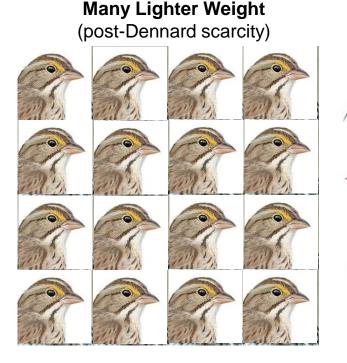
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Specialization: More Performance in a Resource-Limited Environment

Powerful General Purpose





Many Different Specialized (Post-Moore Scarcity) Insect catching Probing Filter feeding Coniferous-seed eating Nectar feeding Aerial fishing Pursuit fishing Chiseli Scavenging Raptoria Surface skimming Scything

Xeon, Power

KNL AMD, Cavium/Marvell, GPU

Apple, Google, Amazon



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Already Happening in Datacenters and Consumer Electronics



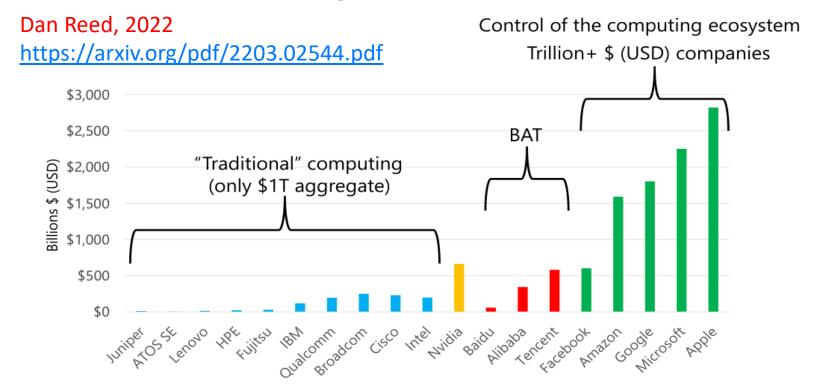
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Google TPU: Deployed in datacenters since 2015





But Those Are Large Markets





Opportunity for HPC: New Economic Model

- Open Chiplets Marketplace is forming (ODSA and UCIexpress)
 - Licensable IP and assembly by 3rd party lowers that barrier
- Leverage this baseline and extend to support HPC
 - Smaller incremental cost for HPC to "play"
 - HPC has become "too small to attack the city"

Leverage commodity ecosystems

- Get licensed IP where there is a market to support it
- Use open-source IP where the government needs to develop technology to serve its needs
- Partner with chip vendors to realize systems

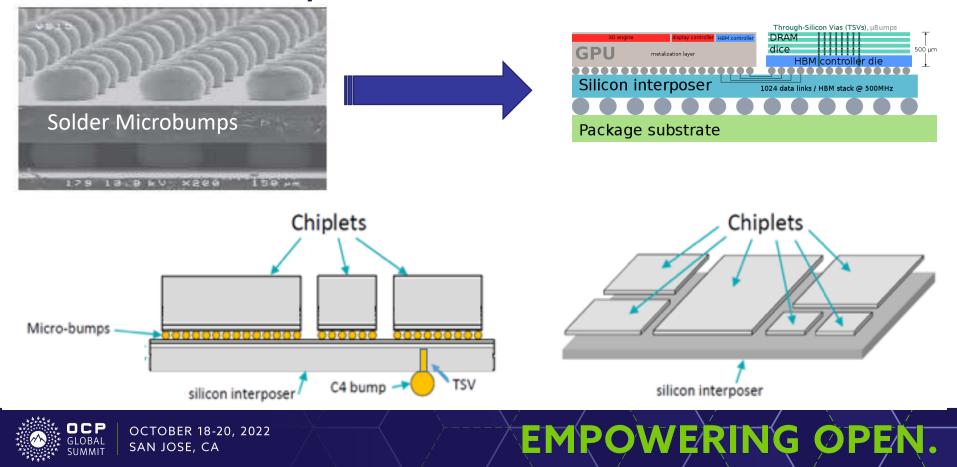


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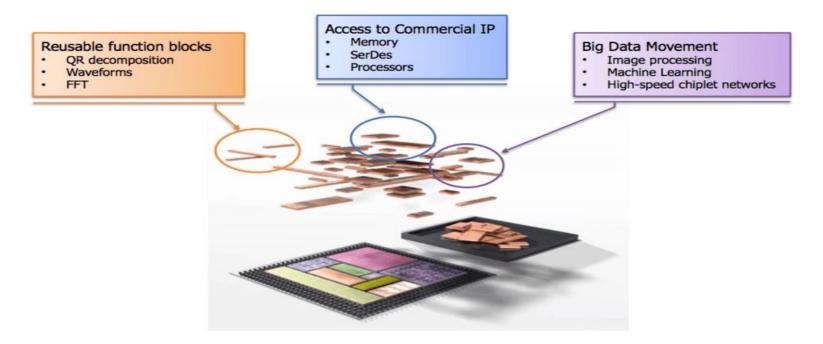
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What is a Chiplet?



How Do Chiplets Help?

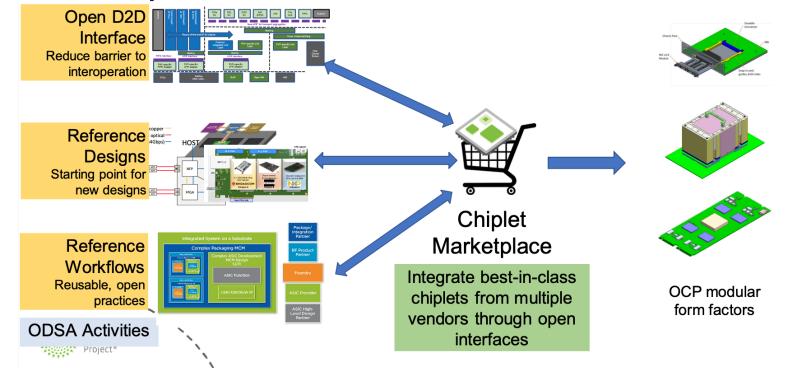


CHIPS modularity targets the enabling of a wide range of custom solutions



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This is Already Happening in <u>ODSA</u>: Open Domain Specific Architecture





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[Call to Action]

- Support the development of open-source hardware IP for HPC
- Identify which compute accelerators are HPC-specific and thus should be open source, and which commercialized
- Develop design system-wide management methods to reduce underutilization

- Develop programming abstractions and accelerator usage libraries
- Contact the speaker: mihelog@lbl.gov



Thank you!

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